

ABSTRACT OF THE DISCLOSURE

Circuit board 10 is of substantially rectangular shape, provided in the upper half of the juxtaposed face 13 with a substantially circular test terminal 20. In the lower half are provided a plurality of substantially rectangular terminals 21 -27, arrayed in two rows, i.e., an upper and lower row, the upper row containing an I/O terminal 21 for data input/output, a power supply terminal 22 for supplying power, and a chip select terminal 23 for input of a chip select signal CS. The lower row of juxtaposed face 13 contains a ground terminal 24, a read/write terminal 25 for inputting read/write control signals W/R, a clock terminal 26 for inputting a clock signal CLK, and a ground terminal 27.